Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belagavim)

Department of Electronics & Communication Engineering

Ref.No: AIT /EC /BOS /   /2018-19   Date: 23-06-2018

To
Dean (Academics)
Dr Ambedkar Institute of Technology
Bengaluru-56

Madam

Sub: Regarding the details of the BOS meeting held on 23-06-2018
Ref: AIT/Dean / 1550 / 2016718 Dated: 05.06.2018

The BOS meeting of the department was held in the VLSI Lab Hall of our department on Saturday, the 23-06-2018

The BOS committee has approved the following:

1. The Curriculum Design for all the semesters (I to VIII semesters) of UG Course for the batch 2018-19.
2. The syllabus of Basic Electronics for the I/II Semester of UG Courses for applicable to 2018-19 Batch.
3. The Scheme and Syllabus for all the semesters (III to VIII semesters) of UG Course for the Batch 2015,2016 and 2017.
4. The List of BOE members.
5. The list of Valuers / Examiners.
6. The subject Equivalence.

Thanking you

CHAIRMAN BOS
Dept.ECE

Enclosures:

1. List of Members of BOS.
2. Curriculum Design –UG
3. Minutes of the BOS Meeting.
4. Syllabus copy of I/II Semester Basic Electronics for the academic year 2018-19.
5. Scheme of 3rd to 8th Semesters B.E (E &C) for applicable to 2018-19 Batch.
6. Scheme and Syllabus of 3rd to 8th Semesters B.E (E &C) for the academic year 2018-19.
7. Syllabus of Institute Electives.
8. List of Members of BOE.
9. List of Valuers / Examiners.
10. List of Subject Equivalence.

MINUTES OF THE BOARD OF STUDIES MEETING

DATED: Saturday, June 23,2018

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belagavim)

Department of Electronics & Communication Engineering

Minutes of the Board of Studies (BOS) Meeting:

The proceedings of the 8th Board of Studies (BOS) meeting of Electronics and Communication Engineering Department was held on 23-06-2018 at 09:30 a.m. at ECE department under the Chairmanship of Dr.G.V.Jayaramaiah, Professor and Head of the Department of Electronics and Communication Engineering.

At the very outset, the Chairman welcomed all the Internal and External members of BOS to the meeting and gave a preliminary presentation on the agenda items with reference to the scheme and syllabus of UG for the academic year 2018-19

The chairman along with academic coordinator(s) gave a detailed presentation of the courses to be offered to the UG level students of both Core and Elective subjects in a semester wise and also briefed the members about the Curriculum Design of the Department for the UG Course.

The following agenda were placed by the Chairman which were discussed and resolved as follows:

Agenda 1: The confirmation of the proceedings of 7th BOS meeting held on 25-03-2017 at 10 am

The 7th BOS meeting proceedings were confirmed.
Discussion: The members appreciated the progress during the academic year 2017-18.

Agenda 2: To frame, discuss and approve the Curriculum Design for the semesters I to VIII of UG Course for the academic year 2018-19 onwards under **Choice Based Credit System** and **Outcome Based Education System.**

The Chairman appraised the members about the introduction of Choice Based Credit System and Outcome Based Education System for 4 year course in Electronics and Communication Engineering with the course Matrix.

Accordingly, a draft Curriculum Design for the I to VIII Semesters was presented and placed before the **Board Of Studies** Members for their opinion and approval.

Resolution: The BOS members went through the Curriculum Design and discussed in length about various aspects of the Curriculum Design. After Incorporating, the changes suggested by the members of BOS, the **Curriculum Design for I to VIII Semesters** was approved 2018-19 onwards.

Agenda 3: To frame, discuss and approve the syllabus of Basic Electronics for I and II Semester for the academic year 2018-19.

The Chairman presented the Syllabus of the Basic Electronics for the First and Second Semester students of all the branches of Engineering for the academic year 2018-19. Accordingly, a draft syllabus for the First and Second Semesters was presented and placed before the Board Of Studies Members for their opinion and approval.

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Department of Electronics & Communication Engineering

Resolution: The BOS members went through the Syllabus of Basic Electronics and discussed in length about the various aspects of the syllabus. After Incorporation of the Changes as suggested by the members of BOS, the **Basic Electronics syllabus** for I and II Semesters was approved for the academic year 2018-19.

Agenda 4: To frame, discuss and approve the Scheme for III to VIII Semester under Choice Based Credit System and Outcome Based Education System for 4 Years Course in Electronics and Communication Engineering for the academic year 2018-19 onwards.

The BOS Chairman presented the Scheme for III to VIII Semester under Choice Based Credit System and Outcome Based Education System for 4 Years Course in Electronics and Communication Engineering for the academic year 2018-19 onwards and placed before the Board Of Studies Members for their opinion and approval.

Resolution: The BOS members went through the draft Schemes thoroughly for the III to VIII Semester and discussed in length about various aspects of the title of the subjects and the laboratory. After incorporating certain Changes in the title of subjects as suggested by the committee members of BOS, the **Scheme** for III to VIII Semesters was approved for the academic year 2019-20 onwards under Outcome Based Education System and Choice Based Credit System.

Agenda 5: To frame, discuss and approve the Scheme and Syllabus for III to VIII Semester under Choice Based Credit System and Outcome Based Education System for 4 Years Course in Electronics and Communication Engineering for the academic year 2018-19 onwards and syllabus of the Institute electives offered by the other department students of the college.

The BOS Chairman presented the Scheme and Syllabus for III to VIII Semester for 4 Years Course in Electronics and Communication Engineering for the academic year 2018-19 and placed before the Board Of Studies Members for their opinion and approval.

Resolution: The BOS members went through the draft Schemes and Syllabus thoroughly for the III to VIII Semester applicable to 2015-16 Batch, 2016-17 Batch and 2017-18 Batch and discussed in length about various aspects of the syllabus of theory and laboratory. After incorporating the certain Changes in the Computer Communication Network and Computer Communication Network Laboratory subjects as suggested by the committee members of BOS, the **Scheme and Syllabus** for III to VIII Semesters was approved for the academic year 2018. The members of the BOS committee have also approved the syllabus of the Institute electives for the academic year 2018-19.

Agenda 6: To prepare the panel for the Board of Examiners of the Electronics and Communication Engineering to scrutinize the Question papers of the Semester End Examination for the AY: 2018-19.

The BOS Chairman presented the list of Examiners to scrutinize the question papers for the Semester End Examination for the academic year 2018-19.

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Department of Electronics & Communication Engineering

Resolution: As suggested by the members of the BOS, the list of the Board of Examiners was approved.

Agenda 7: To prepare the panel for the Board of Examiners of the Electronics and Communication Engineering for the academic year 2018-19.

The Chairman of the BOS in E & C department presented the proposed panel of Examiners pertaining to UG in Electronics and Communication Engineering for the academic year 2018-19.

Resoulution: The Panel of Examiners were approved based on the recommendation of BOS members including the changes which were made in the list of Examiners.

Agenda 8: To frame the subject equivalence for the academic year 2018-19.

The Chairman of the BOS in E & C Department presented the proposed subject equivalence pertaining to UG in Electronics and Communication Engineering for the academic year 2018-19.

Resoulution: The members of the BOS approved the subject equivalence for the academic year 2018-19 onwards after discussing in length about various aspects of the subject equivalence.

Finally, the meeting of Board of studies was concluded with the Chairman of the BOS in ECE Department thanking to all the members of the BOS for their active participation in the deliberations of the meeting and giving inputs for the progress of the department.

The following BOS members were present on Saturday, June 23, 2018.

1. Dr.G.V.Jayaramaiah, Professor and HOD, Chairman of BOS
2. Dr.Shanthala, Professor and HOD, TCE, BIT, Bengaluru-4 and VTU Nominee
3. Dr.Sadashivappa, Professor and HOD, TCE, RVCE, Bengaluru and Subject Expert
4. Dr.Sheshachalam, Professor, ECE, BMSCE, Bengaluru-18 and Subject Expert
5. Dr.Raveesh Aradhy, Professor, ECE, RVCE, Bengaluru-59 and Subject Expert
6. Dr.Amit Jain, Associate Professor, ECE, CMRIT, Bengaluru-37 and Subject Expert
7. Premakumar Melukote, Alumni Student Nominee
8. Srinivasa.R.K, Project Manager, Qualcomm India Pvt.Ltd, Industry Representative
9. Vinay Sidhaya, Principal Hardware Engineer, Westen Digital, Industry Representative
10. Dr.H.Umadevi, Professr, BOS Member
11. Akalpita L Kulkarni, Associate Professr, BOS Member
12. Dr.S.Ramesh, Professor, BOS Member
13. Dr.Shivaputra, Assistant Professor, BOS Member
14. Meenakshi L Rathod, Assistant Professor, BOS Member

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Department of Electronics & Communication Engineering

Members Absent:

1. Girish Chandra, TCS, Industry Representative
2. Dr.Debabrata Das, Dean-Academics and R & D, Academic Expert

Signature of the Coordiantor(s)

Signature of the BOS Chairman

1. Dr. S. Sharuthala
   VTU Nominee

2. Dr. Seshachalam D

3. Dr. Sadashivappa.G

4. Dr. Raveesh Aaradya H.V

Subject Title : Computer Communication Networks(CCN)

Sub.Code: EC72   No. of Credits:04=04:0:0 (L - T – P)   No. of Lecture Hours/Week : 04
Exam Duration:03 Hrs   CIE+Assignment +SEE=45+5+50=100   Total No.of Contact Hours:52

Course Learning Objectives: From this course the students can learn

1   Layerig architecture of OSI reference and TCP/IP model.

2   Elaborates the background concepts, and functionalities of application layer, transport layer, and network layer.

3   Study and analyse the flow and error control schemes.

4   Present ample details about the protocols, technologies, algorithms and standards that are used by each layer as it relates to the internet.

5   Overview of LAN concept, link layer,connecting LANs and connecting devices.

Unit No	Syllabus Contents	No.of Hours	Blooms Taxnomy level.
1	**Network Models:** Network core, Protocol Layers and their Service Models. **Addressing:** Physical addressing, Logical addressing, Port addressing and specific addressing. (T2:45-50) **Connecting Devices:** Passive hubs, Repeaters, Active hubs, Bridges, Two-layer Switch, Routers, Three-Layer Switches and Gateway. (T2:445-455) **Network Topologies:** Bus, Star, Mesh, Ring and Hybrid topologies. TEXT 2	10	L1,L2,L3.
2	**The Link Layer: Links, Access Networks, and LANs:** Introduction to the Link Layer, Error-Detection and -Correction Techniques, Multiple Access Links and Protocols, Switched Local Area Networks, Link Virtualization: A Network as a Link Layer TEXT 2	10	L1,L2,L3.
3	**The Network Layer:** Introduction, Virtual Circuit and Datagram Networks, What's Inside a Router? The Internet Protocol (IP): Forwarding and Addressing in the Internet, Routing Algorithms, Routing in the Internet, Broadcast and Multicast Routing TEXT 1, TEXT 2	12	L1,L2,L3.
4	**Transport Layer:** Introduction and Transport-Layer Services, Multiplexing and Demultiplexing,Connectionless Transport: UDP, Principles of Reliable Data Transfer, Connection-Oriented Transport: TCP, Principles of Congestion Control, TCP Congestion Control. TEXT 1	10	L1,L2,L3
5	**Application Layer:** Principles of Network Applications, The Web and HTTP, File Transfer: FTP, Electronic Mail in the Internet, DNS—The Internet's Directory Service, Peer-to-Peer Applications,Socket Programming. Text1	10	L1,L2,L3

Note 1:   Two assignments are evaluated for 5 marks: Assignment -1 from Units 1 and 2.
               Assignment -2 from Units 3, 4 and 5

Course Outcomes: After successful completion this course the student should be able to

CO1 Define the network models, layered tasks and protocols for data transmission.

CO2 Explain the functionalities and services of application layer, transport layer, network layer, and data link layer.

CO3 Compare and contrast between various protocols in each layer.

CO4 Apply the various protocols and functions to create networks.

Course Outcomes Mapping with Programme Outcomes.

CO1 PO1, PO2

CO2 PO1, PO2

CO3 PO1, PO2

CO4 PO1, PO2

Text Books.

1 James F. Kurose, Keith W. Ross, "**Computer Networks**", Pearson Education, 2nd Edition, **2003**.

2 B.Forouzan ," **Data Communication and Networking**", TMH, 4th Edition, *2006*.

3 CCNA routing and Switching study guide.

Reference Text Books.

1 Russel Bradford, "**The Art of Computer Networking**", Pearson Education, Ist Edition, **2007**.

Web Links.

1 nptel.ac.in/courses/106105081/1

2 http://www.bau.edu.jo/UserPortal/UserProfile/PostsAttach/10617_1870_1.pdf

Sub Title: COMPUTER COMMUNICATION NETWORKS LABORATORY		
Sub Code:ECL76	**No of Credits :** 0:0:1.5:0	**No of lecture hours/week :** 3
Exam Duration :03Hours	**Exam Marks : 50**	**Total No of lab : 13**

	Course Learning Objectives(CLOs)	
1.	To be able to perform experiment to simulate stuffing and de-stuffing, Encryption and Decryption of message.	
2.	To be able to perform experiment to simulate spanning tree and shortest path algorithm.	
3.	To be able to perform experiment to simulate polynomial code checksum for CRC-CCITT.	
4.	To be able to perform experiment to simulate various LAN protocols.	
5.	To be able to perform experiment to simulate serial using data communication kit.	

EX. NO.	Syllabus	No of Hours
1	Write a C program to implement Bit Stuffing and deStuffing.	3
2	Write a C program to simulate a character stuffing and destuffing for a given message.	3
3	Write a C program to compute a polynomial checksum for a given binary data frame	3
4	Write a C program to simulate a shortest path Algorithm.	3
5	Using TCP/IP Sockets, write a client-server program to make client to communicate with Server using socket programming techniques in python.	3
	PART-B [Simulation]	
6	**SERIAL COMMUNICATION USING** (i) RS232 (ii) MODEM COMMUNICATION (iii) FIBER OPTIC COMMUNICATION	3
7	**Configuring and Verifying LAYER 2 Switches:** Establish a communication between the HOSTS by connecting the Network Devices as given below, configure them and verify the same. Configuration includes HOSTNAME, BANNER, PASSWORD (CONSOLE, TELNET and ENABLE),MANAGEMENT IPand DEFAULT GATEWAY.	3

BOS Chairman   Signature of Principal

8	**Configuring and Verifying VLAN:**Establish a communication between the hosts by connecting the network Devices as given below, configure them and verify the same. Configuration includes Switch port configuration and encapsulation methods.	3
9	**Configuring and Verifying IP Routing:**Establish a communication between the hosts by connecting the network Devices as given below, configure them and verify the same. Configuration includes: 1. Static Routing 2. Dynamic Routing (RIP/OSPF/EIGRP)	3
10	**Configuring DHCP Server on a Router:** Configure DHCP server on a router to assign IP address dynamically to the hosts and verify the same **a. For One Broadcast Domain** **b. For Many Broadcast Domain**	3
11	**PART-C [Simulation Case-Study]** Dr. AIT is granted a block of addresses starting from **192.168.100.0/24**. The Dr. AIT College committee decided to distribute these blocks of addresses to **THREE** Departments with each department receiving just **FOUR** Addresses. **1. Design the sub blocks and give the slash notation to each sub block.**	3
12	**2. Simulate the above case using Cisco-packet Tracer.** Note: while simulating Consider the following Constraints: a. Establish a communication within the Departments. b. Only HOD's of each Department can communicate (Single user from each Department) with each other.	3
13	Create topology to demonstrate port security using any application layer protocol.	3
	Programme Outcomes (POs)	

1	Able to perform experiment to simulate stuffing /de-stuffing bit frame and character frame data: coded using C/C++.
2	Able to perform experiment to simulate spanning tree and shortest path algorithm: coded using C/C++.
3	Able to perform experiment to simulate polynomial code checksum for CRC-CCITT: coded using C/C++.
4	Able to perform experiment to simulate Encryption and Decryption of message: coded using C/C++.
5	Able to perform experiment to simulate Serial and parallel communication using data communication kit.

Relationship to the Program Outcomes(POs)	
Course Outcomes (COs)	**Program Outcomes (POs)**
CO1	PO5,PO6,PO7
CO2	PO5,PO6,PO7
CO3	PO5,PO6,PO7
CO4	PO5,PO6,PO7
CO5	PO5,PO6,PO7

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Department of Electronics & Communication Engineering

Ref.No: AIT /EC /BOS /   /2019-20   Date: 03-06-2019

To
Dean (Academics)
Dr Ambedkar Institute of Technology
Bengaluru-560 056

Madam

     Sub: Regarding the details of the BOS meeting held on 11-05-2019
     Ref: No Dr.AIT/Dean/003/2018-19 Dated: 22.04.2019

The BOS meeting of the department was held at Seminar Hall S001 in Silver Jubilee Building of our college on Saturday, the 11-05-2019

The BOS committee has approved the following:

1. The Curriculum Design for all the semesters (I to VIII semesters) of UG Course for the batch 2019-20.
2. The syllabus of Basic Electronics for the I/II Semester of UG Courses for applicable to 2019-20 Batch.
3. The Scheme and Syllabus for the V to VIII semesters of UG Course for the Batch 2016,2017
4. The Scheme and Syllabus for III and IV semesters of UG Course for the Batch 2018
5. The List of BOE members for the UG
6. The list of Valuers / Examiners for the UG.
7. The subject Equivalence for the UG
8. List of Subjects identified for the Honorary Degree(Major and Minor)

Thanking you

CHAIRMAN BOS
Dept.ECE

Enclosures:

1. List of Members of BOS.
2. Curriculum Design –UG
3. Minutes of the BOS Meeting.
4. Syllabus copy of I/II Semester Basic Electronics for the academic year 2019-20.
5. Scheme and Syllabus of 5th to 8th Semesters B.E (E &C) for applicable to Batch 2016 and 2017.
6. Scheme and Syllabus of 3rd and 4th Semesters B.E (E &C) for the academic year 2019-20.
7. Syllabus of Institute Electives.(Inter Departmental Electives)
8. List of Members of BOE.
9. List of Valuers / Examiners.
10. List of Subject Equivalence.
11. List of Subjects identified for the Honorary Degree(Major and Minor)

DR. AMBEDKAR INSTITUTE OF TECHNOLOGY, BANGALORE
(An Autonomous Institution Affiliated to VTU, Belgaum)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

LIST OF MEMBERS FOR BOARD OF STUDIES for the Academic year 2019-21
Date:11-05-2019

Sl No.	CATEGORY	Nomination of the Committee	Name of the Person with Designation	Signature
1	Head of the Department	Chairperson	Dr. Jayaramaiah.G.V. Professor and Head Department of ECE, Dr.AIT Bengaluru-56	*(signed)*
2	Faculty Members at Different Levels Bearing Different Specializations	Member 1.	Dr.Umadevi.H Professor. Dept. of ECE, Dr. AIT, Bengaluru-56	OOD
		Member 2.	Dr. Mahalinga V Mandi, Professor Dept of ECE, Dr.AIT, Bengaluru-56	OOD
		Member 3.	Smt. Akalpita L Kulkarni Dept of ECE, Dr. AIT, Bengaluru-56 Assoc. Professor Dept of ECE, Dr AIT, Bengaluru-56	*(signed)* 11/5/19
		Member 4.	Dr. Ramesh.S Professor, Dept of ECE, Dr.AIT, Bengaluru-56, Bengaluru-56	*(signed)* 1.15.19
		Member 5.	Smt. Sudha.B.S. Associate Professor Dept of ECE, Dr.AIT, Bengaluru-56	Sudha.BS. 11/5/19
		Member 6.	Dr.Shivaputra Assistant Professor Dept of ECE, Dr.AIT, Bengaluru-56	*(signed)*
		Member 7.	Dr. Jambunath S Baligar Associate Professor Dept of ECE, Dr.AIT, Bengaluru-56	*(signed)*
		Member 8.	Dr.Chetan.S Assistant Professor, Dept of ECE, Dr.AIT, Bengaluru-56	*(signed)*
3	Subject Experts from outside the College	Member 1.	Dr. Devendra Jalihal Professor, EEE department IIT Madras, Chennai-600 036	*(signed)*

	Nominated by Academic Council	Member 2.	Santanu Mahapatra Professor, Department of Electronic Systems Engineering Indian Institute of Science Bangalore Bengaluru- 560012 INDIA	
		Member 3.	Dr. Mrityunjaya V Latte Principal JSS Academy of Technical Education, Dr. Vishnuvardhan Road, Uttarahalli – Kengeri Main Road, Bengaluru- 560 045, India	
		Member 4.	P.Nagaraju Associate Professor, TCE, RVCE, Bangalore-560 059	
4	Expert from IET Ayodhya	Member 5.	Prof. Ramapati Mishra Director, Former HOD, ECE IET Ayodya, UP	
5	Expert from outside College, Nominated by Vice-Chancellor(VTU)	VTU Nominee	Sri Ram P Hegde Professor and HOD, Department of Electronics and Communication Engineering SDM college of Engineering Ujire-Dharmastala Road, Uijre, Karnataka -574240	
6	Representative from Industry/Corporate Sector/Allied area related to Placement Nominated by Academic council	Member 1.	Narashimamurthy Analog devices India Private Limited Saapuria Nova No.1, Varthur Road Nagavarapalya Bengaluru-560 093	
		Member 2.	Dr. J. Krishna Kishore Senior Scientist, ISRO Satellite Centre Bangalore – 560017 India	ABS
7		Member 3.	Arun Chandrashekar Principal Scientist INTEL Corporation Bengaluru India	ABS

8	Post Graduate Meritorious alumnus nominated by Principal	Member	Mr. Kubendra.K Senior Design Engineer VLSI Group Samsung India,Outer ring Road, Near Marathahalli Bengaluru	
9	Student Representatives	Member	Keerthana N 1DA18EC064 1st Year ECE. UG Student	
10		Member	Vishaka bhat 1DA17ec155 2nd Year, ECE UG Student	
11		Member	Vidhee L 1DA17EC150 2nd Year, ECE UG Student	
12		Member	Sharath N 1DA16EC108 3rd Year, ECE UG Student	
13		Member	Bharath K 1DA16EC144 3rd Year, ECE UG Student	
14		Member	Heera P 1DA16EC041 3rd Year, ECE UG Student	
15		Member	Kumudini G 1DA15EC072 4th Year, ECE UG Student	
16		Member	Pranav Jayaram 1DA15EC105 4th Year, ECE UG Student	
17		Member	Arpitha B V 1DA17LVS02 2nd Year, M.Tech. PG Student	
18		Member	Chinthana K S 1DA17LVS04 2nd Year, M.Tech. PG Student	

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Department of Electronics & Communication Engineering

MINUTES OF THE BOARD OF STUDIES MEETING

DATED: Saturday, May 11, 2019

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belagavim)

Department of Electronics & Communication Engineering

Minutes of the Board of Studies (BOS) Meeting:

The proceedings of the 9th Board of Studies (BOS) meeting of Electronics and Communication Engineering Department was held on 11-05-2019 at 11 a.m. at ECE department under the Chairmanship of Dr.G.V.Jayaramaiah, Professor and Head of the Department of Electronics and Communication Engineering.

At the very outset, the Chairman welcomed all the Internal and External members of BOS to the meeting and gave a preliminary presentation on the agenda items with reference to the scheme and syllabus of UG for the academic year 2019-20

The chairman along with academic coordinator(s) gave a detailed presentation of the courses to be offered to the UG level students of both Core and Elective subjects in a semester wise and also briefed the members about the Curriculum Design of the Department for the UG Course.

The following agenda were placed by the Chairman which were discussed and resolved as follows:

Agenda 1: The confirmation of the proceedings of 8th BOS meeting held on 25-06-2018 at 10 am

The 8th BOS meeting proceedings were confirmed.
Discussion: The members appreciated the progress during the academic year 2018-19.

Agenda 2: To frame, discuss and approve the Curriculum Design for the semesters I to VIII of UG Course for the academic year 2019-20 onwards under Choice Based Credit System and Outcome Based Education System.

The Chairman appraised the members about the introduction of Choice Based Credit System and Outcome Based Education System for 4 year course in Electronics and Communication Engineering with the course Matrix. Accordingly, a draft Curriculum Design for the I to VIII Semesters was presented and placed before the Board Of Studies Members for their opinion and approval.

Resolution: The BOS members went through the Curriculum Design and discussed in length about various aspects of the Curriculum Design. After Incorporating, the changes suggested by the members of BOS, the **Curriculum Design** for I to VIII Semesters was approved 2019-20 onwards.

Agenda 3: To frame, discuss and approve the syllabus of Basic Electronics for I and II Semester for the academic year 2019-20.

The Chairman presented the Syllabus of the Basic Electronics for the First and Second Semester students of all the branches of Engineering for the academic year 2019-20. Accordingly, a draft syllabus for the First and Second Semesters was presented and placed before the Board Of Studies Members for their opinion and approval.

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
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Department of Electronics & Communication Engineering

Resolution: The BOS members went through the Syllabus of Basic Electronics and discussed in length about the various aspects of the syllabus. After Incorporation of the Changes as suggested by the members of BOS. the **Basic Electronics syllabus** for I and II Semesters was approved for the academic year 2019-20.

Agenda 4: To frame, discuss and approve the Scheme for III and IV Semester under Choice Based Credit System and Outcome Based Education System for 4 Years Course in Electronics and Communication Engineering for the academic year 2019-20 onwards.

The BOS Chairman presented the Scheme for III and IV Semester under Choice Based Credit System and Outcome Based Education System for 4 Years Course in Electronics and Communication Engineering for the academic year 201920 onwards and placed before the Board Of Studies Members for their opinion and approval.

Resolution: The BOS members went through the draft Schemes thoroughly for the III to VIII Semester and discussed in length about various aspects of the title of the subjects and the laboratory. After incorporating certain Changes in the title of subjects as suggested by the committee members of BOS, the **Scheme** for III and IV Semesters was approved for the academic year 2019-20 onwards under Outcome Based Education System and Choice Based Credit System.

Agenda 5: To frame, discuss and approve the Scheme and Syllabus for V to VIII Semester under Choice Based Credit System and Outcome Based Education System for 4 Years Course in Electronics and Communication Engineering for the academic year 2019-20 onwards and syllabus of the Institute electives offered by the other department students of the college.

The BOS Chairman presented the Scheme and Syllabus for V to VIII Semester for 4 Years Course in Electronics and Communication Engineering for the academic year 2018-19 and placed before the Board Of Studies Members for their opinion and approval.

Resolution: The BOS members went through the draft Schemes and Syllabus thoroughly for the V to VIII Semester applicable to 2016-17 and Batch, 2017-18 Batch and discussed in length about various aspects of the syllabus of theory and laboratory. After incorporating the certain Changes in the Computer Communication Network and Computer Communication Network Laboratory subjects as suggested by the committee members of BOS, the **Scheme and Syllabus** for III to VIII Semesters was approved for the academic year 2018. The members of the BOS committee have also approved the syllabus of the Institute electives for the academic year 2019-20.

Agenda 6: To prepare the panel for the Board of Examiners of the Electronics and Communication Engineering to scrutinize the Question papers of the Semester End Examination for the AY: 2019-20.

The BOS Chairman presented the list of Examiners to scrutinize the question papers for the Semester End Examination for the academic year 2019-20.

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belagavim)

Department of Electronics & Communication Engineering

Resolution: As suggested by the members of the BOS, the list of the Board of Examiners was approved.

Agenda 7: To prepare the panel for the Board of Examiners of the Electronics and Communication Engineering for the academic year 2019-20.

The Chairman of the BOS in E & C department presented the proposed panel of Examiners pertaining to UG in Electronics and Communication Engineering for the academic year 2019-20.

Resoulution: The Panel of Examiners were approved based on the recommendation of BOS members including the changes which were made in the list of Examiners.

Agenda 8: To frame the subject equivalence for the academic year 2019-20.

The Chairman of the BOS in E & C Department presented the proposed subject equivalence pertaining to UG in Electronics and Communication Engineering for the academic year 2019-20.

Resoulution: The members of the BOS approved the subject equivalence for the academic year 2019-20 onwards after discussing in length about various aspects of the subject equivalence.

Finally, the meeting of Board of studies was concluded with the Chairman of the BOS in ECE Department thanking to all the members of the BOS for their active participation in the deliberations of the meeting and giving inputs for the progress of the department.

The following BOS members were present on Saturday, June 23, 2018.

1. Dr.G.V.Jayaramaiah, Professor and HOD, Chairman of BOS
2. Dr..Sri Ram P Hegde, Professor and HOD, ECE, SDMIT, Ujier, Dakshina kannada District, Karnataka and VTU Nominee
3. Dr.Devendra Jalihal, Professor and HOD, EEE, IIT Madras and Subject Expert
4. Dr.Santanu Mahapara Professor, Electronics and System Design, IISc, Bengaluru-19
5. Professor Ramapati Mishra, Director and Professor at IET Ayodhya, UP and Subject Expert
6. Prof. P.Nagaraj., Associate Professor, ECE, RVCE, Bengaluru-59 and Subject Expert
7. Mr Kubendra, Alumni Student Nominee
8. Mr Narasimha Murthy, Project Manager, Analog Devices and Industry Representative
9. Dr.H.Umadevi, Professr, BOS Member
10. Akalpita L Kulkarni, Associate Professr, BOS Member
11. Dr.S.Ramesh, Professor, BOS Member
12. Dr.Shivaputra, Assistant Professor, BOS Member
13. Dr Jambunath S Baligar, Assistant Professor, BOS Member
14. Dr. Chetan. S, Assistant Professor, BOS Member

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belagavim)

Department of Electronics & Communication Engineering

Members Absent:

1. Dr. J. Krishna Kishore.Senior Scientist., Industry Representative
2. Arun Chandrashekar, Principal Scientist, Industry Representative
3. Dr. Mrityunjaya V Latte,Principal JSS Academy of Technical Education,. Academic Expert
4. Dr.Umadevi, Professor, Internal BOS Member
5. Dr.Mahalinga V Mandi, Professor, Internal BOS Member

Signature of the Coordiantor(s)

Signature of the BOS Chairman

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY

An Autonomous Institution, Aided by Government of Karnataka, Accredited by NAAC
and Affiliated to VTU, Belagavi
BDA Outer Ring Road, Mallathalli,Bengaluru-56

Board Of Studies 2020-21

Proposed UG Scheme and Syllabus
AY: 2020-21

Submitted by
Department of Electronics and Communication Engineering

To
DEAN (Academic)

For Seeking Approval from the Academic Council

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belgaum)

Department of Electronics & Communication Engineering

Members of BOS:

Sl No.	CATEGORY	Nomination of the Committee	Name of the Person with Designation
1	Head of the Department	Chairperson	Dr. Ramesh.S Professor and Head Department of ECE, Dr.AIT, Bengaluru-56
2	Faculty Members at Different Levels Bearing Different Specializations	Member 1.	Dr.Umadevi.H Professor, Department of ECE, Dr.AIT, Bengaluru-56
		Member 2.	Dr. Mahalinga V Mandi, Professor, Department of ECE, Dr.AIT, Bengaluru-56
		Member 3.	Smt. Akalpita L Kulkarni Associate Professor, Department of ECE, Dr.AIT, Bengaluru-56
		Member 4.	Smt. Sudha.B.S. Associate Professor, Department of ECE, Dr.AIT, Bengaluru-56
		Member 5.	Dr.Shivaputra Assistant Professor Department of ECE, Dr.AIT, Bengaluru-56
		Member 6.	Smt. Meenakshi.L.R. Assistant Professor, Department of ECE, Dr.AIT, Bengaluru-56
		Member 7.	Dr. Jambunath S Baligar Associate Professor Department of ECE, Dr.AIT, Bengaluru-56
		Member 8.	Dr. Chetan. S Assistant Professor, Department of ECE, Dr.AIT, Bengaluru-56

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belgaum)

Department of Electronics & Communication Engineering

3	Subject Experts from outside the College Nominated by Academic Council	Member 1.	Dr. Devendra Jalihal Professor, EEE department IIT Madras, Chennai-600 036
		Member 2.	Prof. Santanu Mahapatra Professor, Department of Electronic Systems Engineering, Indian Institute of Science Bangalore, Bengaluru- 560012
		Member 3.	Dr. Mrityunjaya V Latte Principal JSS Academy of Technical Education, Dr. Vishnuvardhan Road, Uttarahalli – Kengeri Main Road, Bengaluru- 560 045
		Member 4.	Prof. P.Nagaraju Associate Professor, Dept. of TCE, RVCE, Bengaluru-560 059
4	Expert from IET Ayodhya	Member 5.	Prof. Ramapati Mishra Director, Former HOD, Dept. of ECE IET Ayodya, UP India
5	Expert from outside College, Nominated by Vice Chancellor (VTU)	VTU Nominee	Dr. Sri Ram P Hegde Professor and HOD, Department of ECE, SDM college of Engineering Ujire-Dharmastala Road, Uijre, Karnataka -574240
	Representative from Industry /Corporate Sector/Allied area related to Placement Nominated by Academic council	Member 1.	Mr. Narashimamurthy Analog devices India Private Limited Saapuria Nova No.1, Varthur Road Nagavarapalya Bengaluru-560 093

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belgaum)

Department of Electronics & Communication Engineering

6		Member 2.	Dr. J. Krishna Kishore Senior Scientist, ISRO Satellite Centre, Bangalore – 560017
		Member 3.	Mr. Arun Chandrashekar Principal Scientist, INTEL Corporation, Bengaluru,
		Member 4.	Mr. Manjunath B Senior Manger HR, Wistron, India Bengaluru,
7	Post Graduate Meritorious alumnus nominated by Principal	Member	Mr. Kubendra.K Senior Design Engineer VLSI Group, Samsung India,Outer ring Road, Near Marathahalli, Bengaluru

CHAIRMAN
BOS Dept. of ECE

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belgaum)

Department of Electronics & Communication Engineering

MINUTES OF THE MEETING

OF

THE

BOARD OF STUDIES 2020-21

DATED: Thursday, 20th August 2020

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belgaum)

Department of Electronics & Communication Engineering

<u>BOS Meeting Notice</u>

Sub: Online Board of Studies Meeting is convened on 20-08-2020

   With reference to the above subject, Online Board of Studies Meeting of the department is convened on Thursday, the 20th August 2020 at 10:30.a.m. on Google Meet online Platform to finalizing the scheme and syllabus of UG in B.E.(E & C) and PG in M.Tech in VLSI Design and Embedded Systems for the academic year 2020-21 with the following agenda.

Agenda:

1. Approval of the Scheme and Syllabus of 1st to 8th Semesters B.E (E &C) for the Batch-2020
2. Approval of the Scheme and Syllabus of 3rd to 8th Semester B.E(E & C) for the Batch -2019
3. Approval of the Scheme and Syllabus of 5th to 8th Semester B.E(E & C) for the Batch-2018
4. Approval of the Scheme and Syllabus of 7th to 8th Semester B.E(E & C) for the Batch-2017
5. Approval to AICTE Syllabus of Basic Electronics (18EC14/24) for 1st /2nd Semester for the Batch 2020.
6. Approval of the Scheme and Syllabus for the 1st and 2nd Semester PG for the Batch-2020
7. Approval of the Scheme and Syllabus for the 3rd and 4th Semester PG for the Batch-2019.
8. Approval of the courses for the Major, Minor Degree
9. Approved to conduct courses and examination for Repeaters in parallel with regular courses and examinations.
10. Approval of List of Examiners

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belgaum)

Department of Electronics & Communication Engineering

Minutes of Board of Studies (BOS) Meeting:

The Meeting of Board of Studies (BOS) of department Electronics and Communication Engineering was held on 20-08-2020 at 10:30 a.m. under the Chairmanship of the Dr. Ramesh S, Professor and Head, Department of Electronics and Communication Engineering on Digital Platform Google Meet with meeting-id.

At the very outset, the Chairman welcomed all the Internal and External members of BOS to the meeting and gave a preliminary presentation on the agenda items with reference to the scheme and syllabus of UG for the academic year 2020-21
The chairman along with academic coordinator(s) gave a detailed presentation of the courses to be offered to the students in both Core and Elective subjects in semester wise at the Under Graduate level and also briefed the members about the Curriculum Design of the Department for the UG Course.

PROCEEDINGS/RESOLUTIONS:

The following are the Suggestions of the members of BOS with reference to the presentations:

➢ Tutorial sessions need to be included for the courses wherever more practice is required.
  Sol. Tutorial Sessions incorporated for Subjects including Numerical.
➢ New technologies like Machine learning, Artificial Intelligence should be introduced in the curriculum.
  Sol. Subjects introduced
➢ Simulation tools relevant to the course need to be integrated.
  Sol. Virtual labs, Xilinx and Cadence have been included in curriculum
➢ Department should play crucial role in placement orientation.
  Sol. Placement and training as made a mandatory non-credit subject in curriculum
➢ Mini projects in courses: Understanding and presentation of relevant IEEE journal papers.
  Sol. More of IEEE journals based and Industry based projects will be implemented as Mini Projects and Major Projects.
➢ Assignment Programs should be added to Python Programming Course.
  Sol. Programming Exercises have been included in appropriate Modules.

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
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Department of Electronics & Communication Engineering

Finally the BOS members approved the following after incorporating the suggested modifications

- ➢ Approved the Curriculum Design for the semesters I to VIII of UG Course for the students of the Batch 2017
- ➢ Approved the Syllabus of Basic Electronics for the semesters I/II of UG Course for the academic year 2020-21.
- ➢ Approved the Scheme and syllabus for all the semesters III to VIII of UG Course for the academic year 2020-21.
- ➢ Approved the Syllabus for Institute Electives offered by the ECE Department.
- ➢ Approved to conduct courses and examination for Repeaters in parallel with regular courses and examinations.
- ➢ Approved the List of BOE members.
- ➢ Approved the list of Valuers / Examiners.

CHAIRMAN
BOS Dept. of ECE

BOS Coordinators   **Signatures**

1. Dr.Shivaputra

2. Mr. Anand H D

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belgaum)

Department of Electronics & Communication Engineering

Attendance List:

Sl No.	CATEGORY	Nomination of the Committee	Name of the Person with Designation	Present/Absent
8	Head of the Department	Chairperson	Dr. Ramesh.S Professor and Head Department of ECE, Dr.AIT, Bengaluru-56	Present
9	Faculty Members at Different Levels Bearing Different Specializations	Member 1.	Dr.Umadevi.H Professor, Department of ECE, Dr.AIT, Bengaluru-56	Present
		Member 2.	Dr. Mahalinga V Mandi, Professor, Department of ECE, Dr.AIT,	Absent
		Member 3.	Smt. Akalpita L Kulkarni Associate Professor, Department of ECE, Dr.AIT,	Absent
		Member 4.	Smt. Sudha.B.S. Associate Professor, Department of ECE, Dr.AIT, Bengaluru-56	Present
		Member 5.	Dr.Shivaputra Assistant Professor Department of ECE, Dr.AIT, Bengaluru-56	Present
		Member 6.	Smt. Meenakshi.L.R. Assistant Professor, Department of ECE, Dr.AIT, Bengaluru-56	Present
		Member 7.	Dr. Jambunath S Baligar Associate Professor Department of ECE, Dr.AIT,	Present
		Member 8.	Dr. Chetan. S Assistant Professor, Department of ECE, Dr.AIT,	Present

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
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Department of Electronics & Communication Engineering

10	Subject Experts from outside the College Nominated by Academic Council	Member 1.	Dr. Devendra Jalihal Professor, EEE department IIT Madras, Chennai-	Absent
		Member 2.	Prof. Santanu Mahapatra Professor, Department of Electronic Systems Engineering, Indian Institute of Science Bangalore,	Present
		Member 3.	Dr. Mrityunjaya V Latte Principal JSS Academy of Technical Education, Dr. Vishnuvardhan Road, Uttarahalli – Kengeri Main Road, Bengaluru- 560 045	Present
		Member 4.	Prof. P.Nagaraju Associate Professor, Dept. of TCE, RVCE, Bengaluru-560 059	Present
11	Expert from IET Ayodhya	Member 1.	Prof. Ramapati Mishra Director, Former HOD, Dept. of ECE IET Ayodya, UP India	Present
12	Expert from outside College, Nominated by Vice Chancellor (VTU)	VTU Nominee	Dr. Sri Ram P Hegde Professor and HOD, Department of ECE, SDM college of Engineering Ujire-Dharmastala Road, Uijre,	Present
	Representative from Industry /Corporate Sector/Allied area related to Placement Nominated by Academic council	Member 1.	Mr. Narashimamurthy Analog devices India Private Limited Saapuria Nova No.1, Varthur Road	Absent
		Member 2.	Dr. J. Krishna Kishore Senior Scientist, ISRO Satellite Centre, Bangalore – 560017	Responded with recommendatio ns through mail.

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
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Department of Electronics & Communication Engineering

		Member 3.	Mr. Arun Chandrashekar Principal Scientist, INTEL Corporation, Bengaluru	Absent
		Member 3	Mr. Arun Chandrashekar Principal Scientist, INTEL Corporation, Bengaluru	Absent
		Member 4	Mr. Manjunath B Senior Manger HR, Wistron, India Bengaluru,	Present
13	Post Graduate Meritorious alumnus nominated by Principal	Member 1.	Mr. Kubendra.K Senior Design Engineer VLSI Group, Samsung India,Outer ring Road, Near Marathahalli, Bengaluru	Present
14	Student Representative	Member 1.	K M Kanchana 2nd Year 1DA19EC056 UG Student	Present
		Member 2.	Shashank S 2nd Year 1DA19EC129 UG Student	Present
		Member 3.	Akhilesh Bagalkot 3rd Year 1DA18EC007 UG Student	Present
		Member 4.	Vaishnavi V Bhat 3rd Year 1DA18EC150 UG Student	Present
		Member 5.	Vishaka Bhat 4th Year 1DA17EC155 UG Student	Present

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
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Department of Electronics & Communication Engineering

		Member 6.	Manoja R 4th Year 1DA17EC076 UG Student	Present
		Member 7.	Meghana S R 2nd Year PG Student	Present
		Member 8.	Manu Mathew 2nd Year PG Student	Present
		Member 9.	Vani K 2nd Year PG Student	Present

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belgaum)

Department of Electronics & Communication Engineering

List of BOE Members:

SL. NO.	NAME AND ADDRESS
1.	Dr. Ramesh S. Professor and Head, dept. of ECE
	External BOE members:
1.	Dr. Dinesh P, Professor, Department of ECE, DSCE, Bengaluru
2.	Nagraj P, Assoc. Professor, Department of TCE, RVCE, Bengaluru
3.	Dr.Rajeshwari Hegade, Professor and Head, Department of TCE, BMSCE, Bengaluru-19
	Internal BOE Members:
1.	Umadevi.H, Assoc. Professor,
2.	Sudha B S, Assoc. Professor
3.	Dr. Shivaputra, Assistant Professor
4.	Meenakshi L R, Assistant Professor
5.	Mohankumar V, Assistant Professor
6.	Girija S, Assistant Professor

CHAIRMAN
BOS Dept. of ECE

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY

(An Autonomous Institute affiliated to VTU, Accredited by NAAC with 'A' grade)

BDA Outer Ring Road, Mallathalli,Bengaluru-56

Board Of Studies 2021-22

Proposed UG
Scheme and Syllabus

For

Academic Year (AY) : 2021-22

Submitted by

Department of Electronics and Communication Engineering

To

DEAN (Academic)

For Seeking Approval from the Academic Council

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belgaum)

Department of Electronics & Communication Engineering

Ref. No: AIT /EC /BOS /২১৭ /2020-21

6|8|২১
Date: 19-06-2021

To
Dean (Academic)
Dr Ambedkar Institute of Technology
Bengaluru-56

Sir,

Sub: Regarding the details of the BOS meeting held on 19-06-2021

The BOS meeting of the department was held through the Digital Platform, Google Meet on Saturday, 19-06-2021 through the meeting link: **meet.google.com/oek-ffdx-ngs**

The BOS committee has approved the following:

1. Approved the syllabus of Basic Electronics for the I/II Semester of UG Courses for the academic year 2021-22 (Batch-2021).
2. Approved the Scheme for all the semesters (III to VIII semesters) of UG Course for the academic year 2021-22.
3. Approved the Syllabus for all the semesters (III to VIII semesters) of UG Course for the academic year 2021-22.
4. Approved the List of BOE members.
5. Approved the list of Valuers / Examiners.

Thanking you

CHAIRMAN BOS
Dept. of ECE

HOD
Dept. of Electronics and Communication Engg.,
Dr. Ambedkar Institute of Technology
Bengaluru - 560056

Enclosures:

1. List of Members of BOS.
2. Curriculum Design –UG
3. Minutes of the BOS Meeting.
4. Scheme & Syllabus of I/II Semester Basic Electronics for the academic year 2021-22.
5. Scheme & Syllabus of 3rd and 4th Semesters for the academic year 2021-22.
6. Scheme & Syllabus of 5th and 6th Semesters for the academic year 2021-22.
7. Scheme & Syllabus of 7th and 8th Semesters for the academic year 2021-22.
8. List of BOE Members.
9. List of valuers / Examiners.

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belgaum)

Department of Electronics & Communication Engineering

Members of BOS:

Sl No.	CATEGORY	Nomination of the Committee	Name of the Person with Designation
1	Head of the Department	Chairperson	Dr. Ramesh.S Professor and Head Department of ECE, Dr.AIT, Bengaluru-56
2	Faculty Members at Different Levels Bearing Different Specializations	Member 1.	Dr.Umadevi.H Professor, Department of ECE, Dr.AIT, Bengaluru-56
		Member 2.	Dr. Mahalinga V Mandi, Professor, Department of ECE, Dr.AIT, Bengaluru-56
		Member 3.	Smt. Sudha.B.S. Associate Professor, Department of ECE, Dr.AIT, Bengaluru-56
		Member 4.	Dr.Shivaputra Assistant Professor Department of ECE, Dr.AIT, Bengaluru-56
		Member 5.	Smt. Meenakshi.L.R. Assistant Professor, Department of ECE, Dr.AIT, Bengaluru-56
		Member 6.	Mr. Mohan Kumar V Assistant Professor, Department of ECE, Dr.AIT, Bengaluru-56
		Member 7.	Dr. Jambunath S Baligar Associate Professor Department of ECE, Dr.AIT, Bengaluru-56
		Member 8.	Dr. Chetan. S Assistant Professor, Department of ECE, Dr.AIT, Bengaluru-56

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
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Department of Electronics & Communication Engineering

3	Subject Experts from outside the College Nominated by Academic Council	Member 1.	Dr. Devendra Jalihal Professor, EEE department IIT Madras, Chennai-600 036
		Member 2.	Prof. Santanu Mahapatra Professor, Department of Electronic Systems Engineering, Indian Institute of Science Bangalore, Bengaluru- 560012
		Member 3.	Dr. Mrityunjaya V Latte Principal JSS Academy of Technical Education, Dr. Vishnuvardhan Road, Uttarahalli – Kengeri Main Road, Bengaluru- 560 045
		Member 4.	Prof. P.Nagaraju Associate Professor, Dept. of TCE, RVCE, Bengaluru-560 059
4	Expert from IET Ayodhya	Member 5.	Prof. Ramapati Mishra Director, Former HOD, Dept. of ECE IET Ayodya, UP India
5	Expert from outside College, Nominated by Vice Chancellor (VTU)	VTU Nominee	Dr. Sri Ram P Hegde Professor and HOD, Department of ECE, SDM college of Engineering Ujire-Dharmastala Road, Uijre, Karnataka -574240
	Representative from Industry /Corporate Sector/Allied area related to Placement Nominated by Academic council	Member 1.	Mr. Narashimamurthy Analog devices India Private Limited Saapuria Nova No.1, Varthur Road Nagavarapalya Bengaluru-560 093

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
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Department of Electronics & Communication Engineering

6		Member 2.	Dr. J. Krishna Kishore Senior Scientist, ISRO Satellite Centre, Bangalore – 560017
		Member 3.	Mr. Arun Chandrashekar Principal Scientist, INTEL Corporation, Bengaluru,
		Member 4.	Mr. Manjunath B Senior Manger HR, Wistron, India Bengaluru,
7	Post Graduate Meritorious alumnus nominated by Principal	**Member**	Mr. Kubendra.K Senior Design Engineer VLSI Group, Samsung India,Outer ring Road, Near Marathahalli, Bengaluru

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CHAIRMAN

BOS Dept. of ECE

HOD
Dept. of Electronics and Communication Engg..
Dr. Ambedkar Institute of Technology
Bengaluru - 560056

MINUTES OF THE MEETING

OF THE

BOARD OF STUDIES 2021-22

DATED: Saturday, 19th June 2021

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belgaum)

Department of Electronics & Communication Engineering

BOS Meeting Notice

Sub: Online Board of Studies Meeting is convened on 19-06-2021

With reference to the above subject, Online Board of Studies Meeting of the department is convened on Saturday, the 19th June 2021 at 11:00 a.m. on Google Meet online Platform for finalizing the scheme and syllabus of UG in B.E. (E & C) and PG in M.Tech in VLSI Design and Embedded Systems for the academic year 2021-22 with the following agenda.

Agenda:

1. Approval of the Scheme and Syllabus of 1st to 8th Semesters B.E (E &C) for the Batch-2021
2. Approval of the Scheme and Syllabus of 3rd to 8th Semester B.E(E & C) for the Batch -2020
3. Approval of the Scheme and Syllabus of 5th to 8th Semester B.E(E & C) for the Batch-2019
4. Approval of the Scheme and Syllabus of 7th to 8th Semester B.E(E & C) for the Batch-2018
5. Approval to AICTE Syllabus of Basic Electronics (18EC14/24) for 1st /2nd Semester for the Batch 2020.
6. Approval of the Scheme and Syllabus for the 1st and 2nd Semester PG for the Batch-2021
7. Approval of the Scheme and Syllabus for the 3rd and 4th Semester PG for the Batch-2020.
8. Approval of the courses for the Major, Minor Degree
9. Approved to conduct courses and examination for Repeaters in parallel with regular courses and examinations.
10. Approval of List of Examiners

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belgaum)

Department of Electronics & Communication Engineering

Minutes of Board of Studies (BOS) Meeting:

The Meeting of Board of Studies (BOS) of department Electronics and Communication Engineering was held on 19-06-2021 at 11:00 a.m. under the Chairmanship of the Dr. Ramesh S, Professor and Head, Department of Electronics and Communication Engineering on Digital Platform Google Meet with meeting-id: meet.google.com/ock-ffdx-ngs

At the very outset, the Chairman welcomed all the Internal and External members of BOS to the meeting and gave a preliminary presentation on the agenda items with reference to the scheme and syllabus of UG for the academic year 2021-22

The chairman along with academic coordinator(s) gave a detailed presentation of the courses to be offered to the students in both Core and Elective subjects in semester wise at the Under Graduate level and also briefed the members about the Curriculum Design of the Department for the UG Course.

PROCEEDINGS/RESOLUTIONS:

The following are the Suggestions of the members of BOS with reference to the presentations:

➤ Inter-department Elective Subject, Unmanned Automatic Vehicles (UAV) should be introduced in the curriculum.

Sol. Subject 'Introduction to Unmanned Automatic Vehicles (UAV)' with subject code 18ECE13 added to Open Elective-C list.

➤ Syllabus of Inter-department Elective Subject, Television Engineering (18ECE03) must be updated.

Sol. Syllabus of Television Engineering (18ECE03) subject is updated and renamed as Digital Television Engineering.

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU – 560056.
(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belgaum)

Department of Electronics & Communication Engineering

Finally, the BOS members approved the following after incorporating the suggested modifications

- ➢ Approved the Curriculum Design for the semesters I to VIII of UG Course for the students of the Batch 2021

- ➢ Approved the Syllabus of Basic Electronics for the semesters I/II of UG Course for the academic year 2021-22.

- ➢ Approved the Scheme and syllabus for all the semesters III to VIII of UG Course for the academic year 2021-22.

- ➢ Approved the Scheme and syllabus for all the semesters V to VIII of UG Course for the academic year 2021-22.

- ➢ Approved the Scheme and syllabus for all the semesters VII to VIII of UG Course for the academic year 2021-22.

- ➢ Approved the Syllabus for Institute Electives offered by the ECE Department.

- ➢ Approved to conduct courses and examination for Repeaters in parallel with regular courses and examinations.

- ➢ Approved the List of BOE members.

- ➢ Approved the list of Valuers / Examiners.

CHAIRMAN 4.8.21

BOS Dept. of ECE

HOD
Dept. of Electronics and Communication Engg..
Dr. Ambedkar Institute of Technology
Bengaluru - 560056

BOS Coordinators	Signatures
1. Dr.Shivaputra	
2. Mr. Anand H D	